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U.S. UTILITY Patent Application

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ISSUE DATE

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| APPL NUM 10063394 | FILING DATE 04/18/2002 | CLASS 716 270 | SUBCLASS 6 | GAU 2664 | EXAMINER Rossoshek |
| **APPLICANTS: Feng Kai; 2825 | | | | | |
| **CONTINUING DATA VERIFIED: | | | | | |
| BEST AVAILABLE COPY | | | | | |
| ** FOREIGN APPLICATIONS VERIFIED: | | | | | |
| PG-PUB | | DO NOT PUBLISH <input type="checkbox"/> | | RESCIND <input type="checkbox"/> | |
| Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners's Initials | | | | ATTORNEY DOCKET NO BUR920010121 | |
| TITLE : On chip timing adjustment in multi-channel fast data transfer | | | | | |
| <small>U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)</small> | | | | | |

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|--|-----------|---------------------------|--|-----------------------------|-------------------------|
| NOTICE OF ALLOWANCE MAILED | | Assistant Examiner | | CLAIMS ALLOWED | |
| | | | | Total Claims: | Print Claim for O.G. |
| ISSUE FEE | | Primary Examiner | | DRAWING | |
| Amount Due | Date Paid | | | Sheets Drawg. | Figs. Drawg. |
| <input type="checkbox"/> TERMINAL | | PREPARED FOR ISSUE | | Application Examiner | |